

HCMOS/TTL COMPATIBLE CLOCK OSCILLATORS IN HALF SIZE DIP - X008 Series

FEATURES

- RoHS Compliant (Pb-Free), Wide Frequency Range, Industrial and Military Temperature Available
- Very Low Phase Jitter with Fundamental or 3rd Overtone Crystal Design
- Tri-state Output Available, Industry Standard Lead Spacing, Available in Gull Wing Configuration
- Low Cost, Excellent for 16 and 32 Bit MPU's

SPECIFICATIONS

Frequency Range 300 kHz to 125 MHz

Input Voltage (Vcc) $A = +5 \text{ VDC} \pm 10\%$; $B = +3.3 \text{ VDC} \pm 10\%$

Input Current 60 mA Maximum, depending on frequency and output load

Storage Temperature -55°C to 125°C

Overall Frequency Stability

Temperature Range Standard Stability

 $100 = \pm 100 \text{ ppm}$; $50 = \pm 50 \text{ ppm}$; $25 = \pm 25 \text{ ppm}$; $20 = \pm 20 \text{ ppm}$

 $A = 0^{\circ}C$ to $70^{\circ}C$; $B = -40^{\circ}C$ to $85^{\circ}C$; $E = -55^{\circ}C$ to $125^{\circ}C$

 $100A = \pm 100 \text{ ppm} / 0^{\circ}\text{C} \text{ to } 70^{\circ}\text{C}$

Electric Option (Symmetry) 0 = No tristate 60/40%; 2 = No tristate 55/45%; 4 = No tristate 52.5/47.5%

1 = Tristate 60/40%; 3 = Tristate 55/45%; 5 = Tristate 52.5/47.5%

Output Load

Logic "1" / Logic "0" Level Rise/Fall Time (Tr/Tf) HCMOS: Drive up to 50 pF load; TTL: Drive up to 10 TTL gates

0.9Vcc Minimum / 0.1Vcc Maximum 10 ns Maximum - 500 kHz to 25 MHz 6 ns Maximum - 25.10 MHz to 70 MHz 4 ns Maximum - 70.10 MHz to 125 MHz

Start-up time

Enable Time

10 ms Maximum

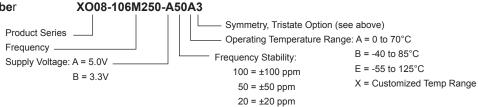
Phase Jitter (RMS, 1 Sigma)

Tristate Function

1 ps Max for fj > 1kHz; 0.3 ps Typical for fj = 12KHz to 20MHz Input (Pin 1) High (> 2.2V) or open: Output (Pin 8) active Input (Pin 1) Low (< 0.8V): Output disabled in high impedance

100 ns Maximum

Creating a Part Number



OUTLINE DRAWING

